Revision. 2

MOS FET

FK8V03030L

Panasonic

FK8V03030L

Silicon N-channel MOSFET

For lithium-ion secondary battery protection circuit For DC-DC Converter

■ Features

- Low drain-source On-state Resistance : RDS(on) typ = 8 m Ω (VGS = 4.5 V)
- High-speed switching : Qg = 10.2 nC
- Halogen-free / RoHS compliant (EU RoHS / UL-94 V-0 / MSL:Level 1 compliant)
- Marking Symbol: 3C

■ Packaging

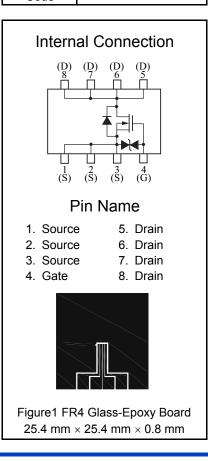
Embossed type (Thermo-compression sealing): 3 000 pcs / reel (standard)

		U	nit: mm
2. 9			
8 + 7 +	0.3		0.16
	-	5.8	
		<u> </u>	
0.65	-	(0. 81)	
1. Source	5	. Drain	
2. Source	e 6	. Drain	1
3. Source	e 7	. Drain	1
4. Gate	8	. Drain	
Panasonic	W	Mini8-F	1
JEITA	SC-115		
Code			

Parameter	Symbol	Rating	Unit	
Drain-source Voltage	VDS	33	V	
Gate-source Voltage	VGS	±20	V	
Drain Current (Steady State) *1	te) *1			
Drain Current (t = 10 s) *1	טו	14		
Drain Current (Pulsed) *1,*2	IDp	48	Α	
Source Current (Pulsed)	ISp	12		
(Body Diode) *1,*2	(BD)	12		
Total Power Dissipation (Steady State) *1	PD	1	W	
Total Power Dissipation (t = 10 s) *1	וו	1.5	VV	
Channel Temperature	Tch	150	ç	
Operating Ambient Temperature	Topr	-40 to +85	°C	
Storage Temperature Range	Tstg	-55 to +150	°C	

Note) *1 Device mounted on a glass-epoxy board (See Figure 1)

*2 Pulse test: Ensure that the channel temperature does not exceed 150°C.



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■ Electrical Characteristics Ta = 25°C ± 3°C

Static Characteristics

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Drain-source Breakdown Voltage	VDSS	ID = 1 mA, VGS = 0 V	33			V
Zero Gate Voltage Drain Current	IDSS	VDS = 33 V, VGS = 0 V			10	μΑ
Gate-source Leakage Current	IGSS	VGS = ± 16 V, VDS = 0 V			±10	μΑ
Gate-source Threshold Voltage		ID = 1.73 mA, VDS = 10 V	1		2.5	V
		ID = 6 A, VGS = 10 V		5	7	mΩ
	RDS(on)2	ID = 6 A, VGS = 4.5 V		8	13	

Dynamic Characteristics

Input Capacitance	Ciss	VDS = 10 V, VGS = 0 V	1100	
Output Capacitance	Coss	f = 1 MHz	250	pF
Reverse Transfer Capacitance	Crss	1 - 1 1011 12	150	
Turn-on Delay Time *2	td(on)	VDD = 15 V, VGS = 0 to 10 V	12	
Rise Time *2	tr	ID = 6 A	7	no
Turn-off Delay Time *2	td(off)	VDD = 15 V, VGS = 10 to 0 V	61	ns
Fall Time *2	tf	ID = 6 A	38	
Total Gate Charge	Qg	VDD = 15 V, VGS = 0 to 4.5 V,	10.2	
Gate-source Charge	Qgs	ID = 12 A	3.1	nC
Gate-drain Charge	Qgd	1D - 12 A	4.7	

Body Diode Characteristic

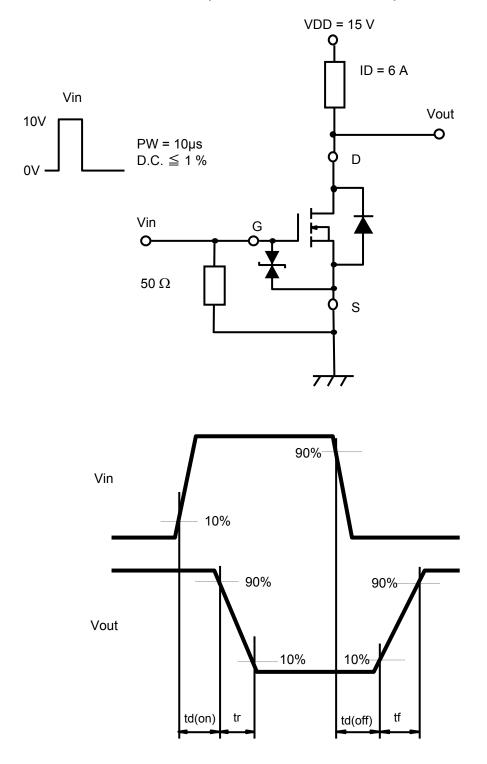
Diode Forward Voltage *1	VSD	IS = 6 A, VGS = 0 V	8.0	1.2	V

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 Measuring methods for transistors.

^{2. *1} Pulse test: Ensure that the channel temperature does not exceed 150°C.

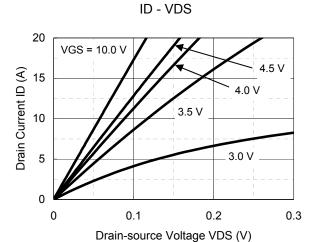
^{*2} Measurement circuit for Turn-on Delay Time/Rise Time/Turn-off Delay Time/Fall Time

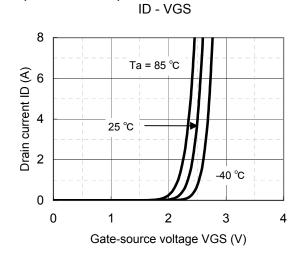
*2 Measurement circuit for Turn-on Delay Time/Rise Time/Turn-off Delay Time/Fall Time

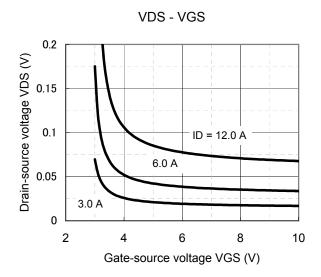


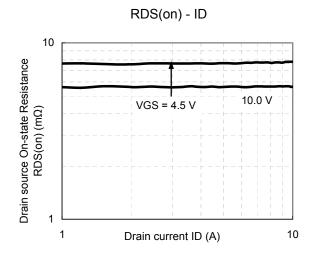
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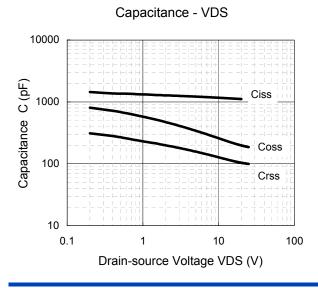
Technical Data (reference)

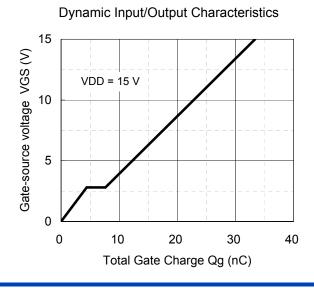










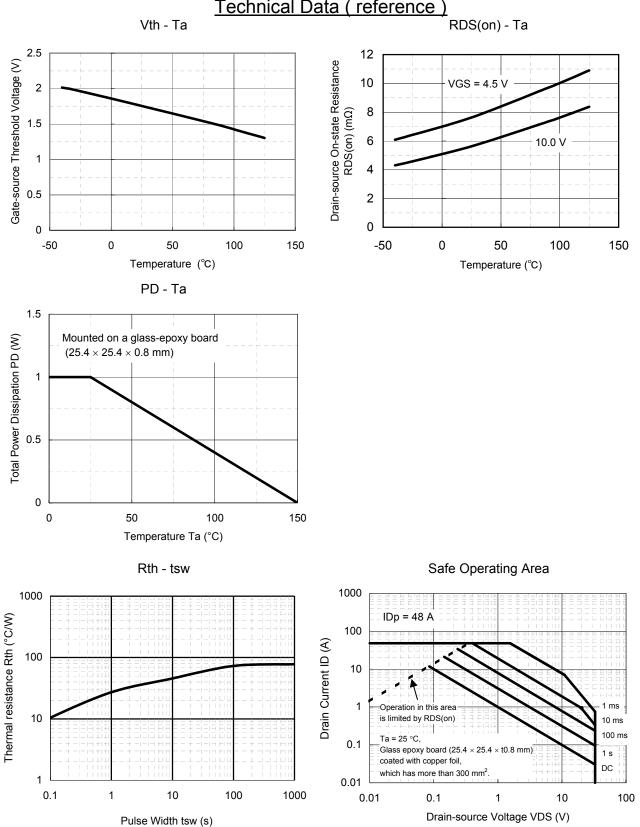


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Technical Data (reference)



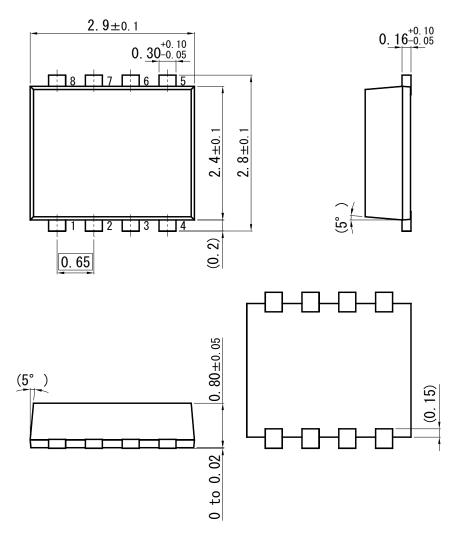
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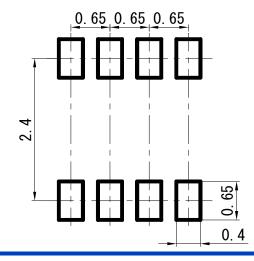
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WMini8-F1

Unit: mm



■ Land Pattern (Reference) (Unit: mm)



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